JEST for CO TINUED EXAMINATION (RCE) TRANSMITTAL

Randwarkx et al.

Examiner:

Maldonado, J.

Ree 2813

Application No.:

09/854,403

Group Art Unit:

2823

Filed:

Title:

May 11, 2001

Docket No.:

PHN 16,741A (VLSI.411PA)

Semiconductor Device with a Bipolar Transistor, and Method of

Manufacturing Such a Device

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 21, 2004.

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

- 1. This is a request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.
- 2. Enclosed please find:
  - a. An Amendment with RCE (4 pgs), submitted herewith to advance the abovereferenced patent application under final rejection.
    - Return Postcard
- 3. Fees
  - a. Please charge Deposit Account Number 50-0996 (VLSI.411PA) the below RCE filing fee, with authorization to charge/credit said Account additional required fees/overages to complete this filing:
    - 1. The RCE LARGE ENTITY filing fee of \$790.00.

Respectfully Submitted,

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